



Product Change Notification / SYST-07NBMH487

Date:

11-Oct-2022

Product Category:

Microprocessors

PCN Type:

Document Change

Notification Subject:

Data Sheet - SAMA5D4 Series

Affected CPNs:

[SYST-07NBMH487_Affected_CPN_10112022.pdf](#)

[SYST-07NBMH487_Affected_CPN_10112022.csv](#)

Notification Text:

SYST-07NBMH487

Microchip has released a new Datasheet for the SAMA5D4 Series of devices. If you are using one of these devices please read the document located at [SAMA5D4 Series](#).

Notification Status: Final

Description of Change: This revision includes the following changes

Section 12. Standard Boot Strategies:	updated Section 12.4.3 Valid Code Detection.
Section 40. Two-wire Interface (TWI):	added important note and THRES field in Section 40.8.12 TWI Filter Register.
Section 56. Electrical Characteristics:	Added Table 56-2 Recommended Thermal Operating Conditions and Package Characteristics.
	Updated Section 56.20.2 Timing Constraints in Section 56.20 Ethernet MAC (GMAC) Timings.
	Updated equation in Section 56.15 SPI Timings
	Updated Table 56-49 Ethernet MAC MDIO Interface Timings.

Section 58. Schematic Checklist:	deleted section TWI EEPROM Boot.
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Impacts to Data Sheet: None

Change Implementation Status: Complete

Date Document Changes Effective: 11 Oct 2022

NOTE: Please be advised that this is a change to the document only the product has not been changed.

Markings to Distinguish Revised from Unrevised Devices: N/A

Attachments:

[SAMA5D4 Series](#)

Please contact your local [Microchip sales office](#) with questions or concerns regarding this notification.

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Affected Catalog Part Numbers (CPN)

ATSAMA5D41A-CU
ATSAMA5D43A-CU
ATSAMA5D41B-CU
ATSAMA5D43B-CU
ATSAMA5D44A-CU
ATSAMA5D42A-CU
ATSAMA5D42B-CU
ATSAMA5D44B-CU
ATSAMA5D41A-CUR
ATSAMA5D43A-CUR
ATSAMA5D41B-CUR
ATSAMA5D43B-CUR
ATSAMA5D42A-CUR
ATSAMA5D44A-CUR
ATSAMA5D42B-CUR
ATSAMA5D44B-CUR