

PCN #245
Notification Date:
October 28, 2022

Product / Process Change Notice

Devices affected:

General purpose rectifiers manufactured in the SMA case.

Part Numbers Affected:

CMR1-005M	CMR1-04M
CMR1-01M	CMR1-06M
CMR1-02M	CMR1-10M

Extent of change:

Additional assembly site

Reason for change:

As part of Central Semiconductor’s supply chain risk mitigation initiative, in an effort to ensure an undisrupted supply of product, an additional assembly site is being added for the referenced deviceS. Product specifications, quality and reliability are not impacted by this addition.

Effect of change:

This addition does not affect the fit, form or function of devices.

Qualification data:

Test	Condition	Duration	Failure rate
Temperature Cycling (T.C.)	TA= -55+0°C/-10°C 10min(Min) TA= +150+15°C/-0°C 10min(Min) Transfer time less than 1min. The load should reach temp. within 15min	1000 Cycles	0/77
High Temp. Storage Life (H.T.S.L.)	TA= +150°C	1000 Hours	0/77
Resistance to Solder Heat (R.S.H.)	T =260°C ±5°C Dwell time = 10 sec.	1 Cycle	0/5
Temperature and Humidity Bias Test (T.H.B.)	TA=+85°C, RH=85%, Bias per Data Sheet	1000 Hours	0/77
Test	Condition	Duration	Failure rate
High Temp. Reverse Bias (H.T.R.B.)	TA=+100°C, VR=80%VB DC Supply	1000 Hours	0/77



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Intermittent Operating Life (I.O.L.)	TA=+25°C, Tj>=100°C, Bias per Data Sheet 5min=time on; 5min= time off	6000 Cycles	0/77
Thermal Shock Test (T.S.T.)	TA=-65°C (5 min) ~ +150°C (5 min)	100 Cycles	0/77
Unbiased Highly Accelerated Temperature and Humidity Stress Test (U.H.A.S.T.)	TA=+130°C, RH=85%	96 Hours	0/77
Solderability (S.D.)	Temperature of Solder Pot=245°C ±5°C Dip Time in Pot 5 sec ±0.5 sec	1 Cycle	0/15

Earliest effective date of change:

October 21, 2022

Sample availability:

Please contact your salesperson or manufacturer’s representative for samples.

As per JEDEC standard JESD46, Customer Notification of Product/Process Changes by Solid-State Suppliers, a lack of acknowledgement of a PCN within thirty (30) days constitutes acceptance of the change.

The undersigned acknowledges and accepts Central Semiconductor’s Product/Process Change Notification (PCN).

Company Name:	
Address:	
Printed Name:	
Title:	
Signature:	
Date:	