



Product Change Notification / SYST-20KVKJ975

Date:

23-Mar-2023

Product Category:

LIN System-in-Package

PCN Type:

Document Change

Notification Subject:

Data Sheet - ATSAMHA1GXXA - Low-power Automotive SiP Product with 32-bit ARM® Cortex® M0+ Processor and LIN SBC

Affected CPNs:

[SYST-20KVKJ975_Affected_CPN_03232023.pdf](#)
[SYST-20KVKJ975_Affected_CPN_03232023.csv](#)

Notification Text:

SYST-20KVKJ975

Microchip has released a new Datasheet for the ATSAMHA1GXXA - Low-power Automotive SiP Product with 32-bit ARM® Cortex® M0+ Processor and LIN SBC of devices. If you are using one of these devices please read the document located at [ATSAMHA1GXXA - Low-power Automotive SiP Product with 32-bit ARM® Cortex® M0+ Processor and LIN SBC](#).

Notification Status: Final

Description of Change:

- Updated chapter Product Identification System Clarified Temperature range of devices in the G17 family.
- Updated Package Marking Section.
- Updated chapter DMA Operation in DMAC Functional description. Removed reference to non-existent bit CHCTRLA.DMAOS.
- Updated chapter Crystal oscillator in Chapter Schematic Checklist. Increased max frequency of crystal oscillator to match Electrical Characteristics.
- Updated register PCLKSR in Chapter SYSCTRL. Corrected bit description for B33SRDY.
- Updated registers DPLLRTIO and DPLLCTRLB in Chapter SYSCTRL. Added formulas to the bitfields that were only present in the textual description of that chapter.
- Updated register GENDIV in Chapter GCLK. Added more verbose description of bitfield DIV.
- Updated chapter XOSC Characteristics Clarified cycles as OSCULP32K cycles.
- Updated chapter DAC Characteristics Removed startup time for VDDANA < 2.6V.
- Updated chapter Data transmission in chapter SERCOM USART to improve the description of the Interrupt flags.
- Updated chapter NVM User Row Mapping removing reference to non-existent package WLCSP.
- Updated chapter PINCFG Fixing reset value of DRVSTR bit.

Impacts to Data Sheet: See above details.

Reason for Change: To Improve Productivity

Change Implementation Status: Complete

Date Document Changes Effective: 23 Mar 2023

NOTE: Please be advised that this is a change to the document only the product has not been changed.

Markings to Distinguish Revised from Unrevised Devices::N/A

Attachments:

[ATSAMHA1GXXA - Low-power Automotive SiP Product with 32-bit ARM® Cortex® M0+ Processor and LIN SBC](#)

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Affected Catalog Part Numbers (CPN)

ATSAMHA1G16A-MBT-BVAO
ATSAMHA1G15A-MBT-BVAO
ATSAMHA1G14A-MBT-BVAO
ATSAMHA0G16A-MZT-BVAO
ATSAMHA0G15A-MZT-BVAO
ATSAMHA0G14A-MZT-BVAO
ATSAMHA1G16A-MBT-BV01
ATSAMHA1G15A-MBT-BV02
ATSAMHA1G16A-MBT-BV03
ATSAMHA1G16A-MBT-BV04
ATSAMHA1G16A-MBT-BV06
ATSAMHA1G15A-MBT-BV07
ATSAMHA1G16A-MBT-BV09
ATSAMHA0G17A-MZT-BV09
ATSAMHA0G17A-MZT-BVAO
ATSAMHA1G17A-MZT-BVAO
ATSAMHA0G17A-MZT-BVAO-GM
ATSAMHA0G17A-MZT-BV09-GM